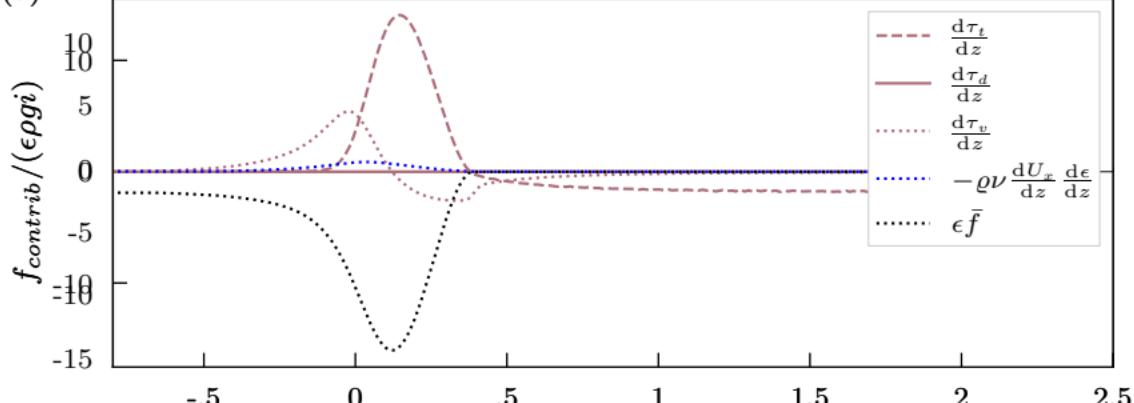


- Exp. - L12

vD + L & S

(a)



(b)

